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PATENT

Docket No. P1296

IN THE

UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S):

BIN YU

SERIAL NO.:

09/826,472

EXAMINER: RON E. POMPEY

FILED:

APRIL 4, 2001

ART UNIT: 2812

FOR:

METHOD OF FABRICATING A SEMICONDUCTOR DEVICE HAVING A NITRIDE/HIGH-K/NITRIDE GATE DIELECTRIC STACK BY ATOMIC LAYER DEPOSITION (ALD) (as herein

amended)

BOX NON-FEE AMENDMENT COMMISSIONER FOR PATENTS WASHINGTON, DC 20231

FORMAL DRAWINGS LETTER

Dear Sir:

Enclosed herewith are two (2) sheets of formal drawings for the above-referenced patent application. There are six (6) figures represented on these two (2) sheets. No new matter has been introduced.

Respectfully submitted,

May Lin DeHaan

Reg. No. 42,472

MLD/pa

Date:October 29, 2002

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